

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: John X Zhong et al.

Application No.: 10/620,628

Art Unit.: 2825

Filed: 07/15/2003

Examiner: Stacy Whitmore

For: "Design Verification By Symbolic Simulation Using A Native Hardware Description Language"

Docket No.: SYN-0551CON1

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: August 24, 2006

REPLY BRIEF TRANSMITTAL

1. Transmitted herewith is a Reply Brief for this application.
2. **STATUS:** Applicant is other than a small entity.
3. **FEE FOR CLAIMS:** The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated below:

(Col.1)	(Col. 2)	(Col. 3)	LARGE ENTITY	
Claims				
Remaining	Highest No.			
After	Previously	Present		
Amendment	Paid For	Extra	Rate	Addit. Fee
Total	16	Minus	20	= -0- x \$50 = \$-0-
Indep.	2	Minus	3	= -0- x \$200 = \$-0-
First Presentation of Multiple Dependent Claim			+ \$360 =	\$0
			Total Addit. Fee	\$-0-

5. **FEE DEFICIENCY:** If any additional fee is required, please charge Deposit Account No. 50-0574.


SIGNATURE OF PRACTITIONER
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